

Fig. 1

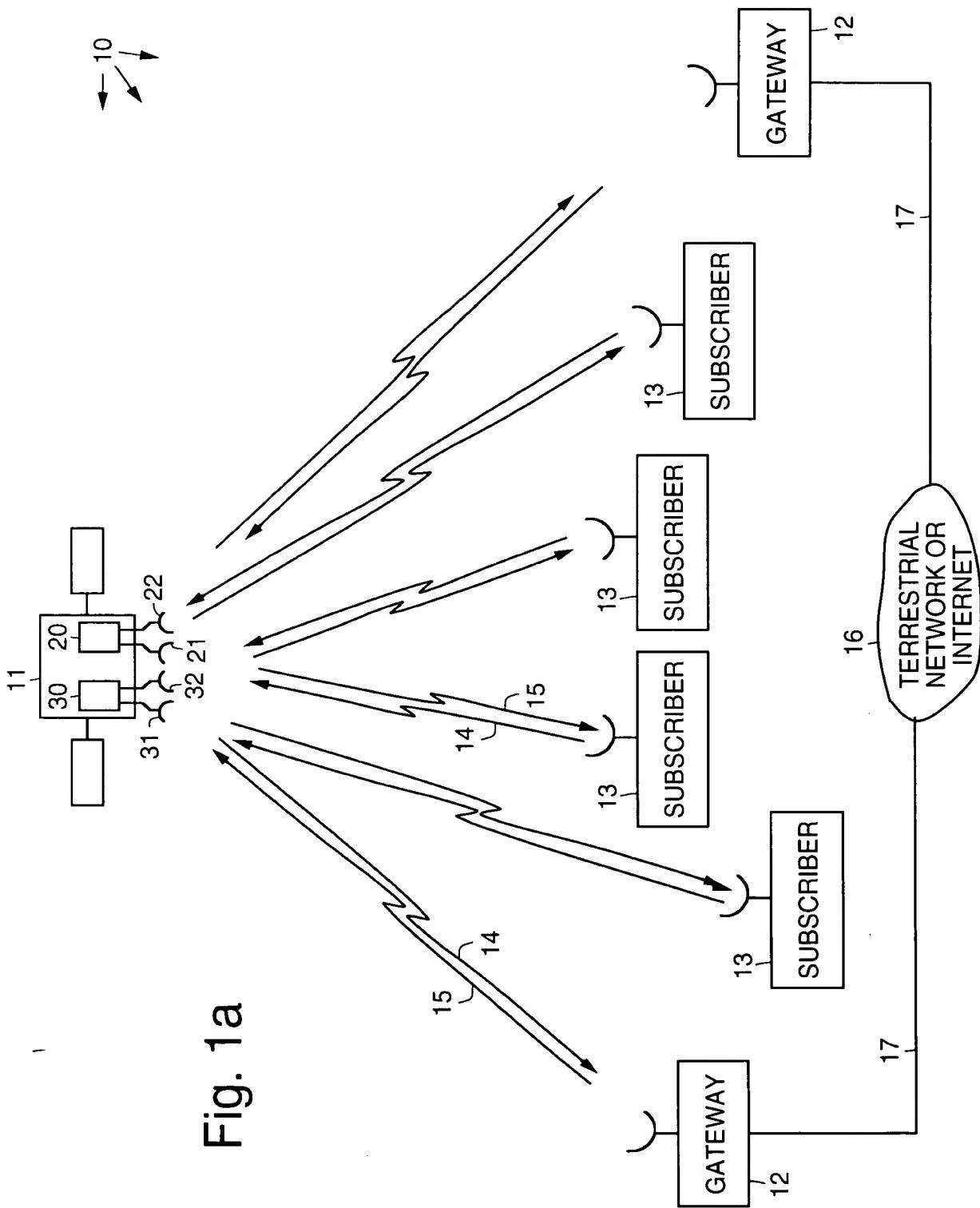


Fig. 1a

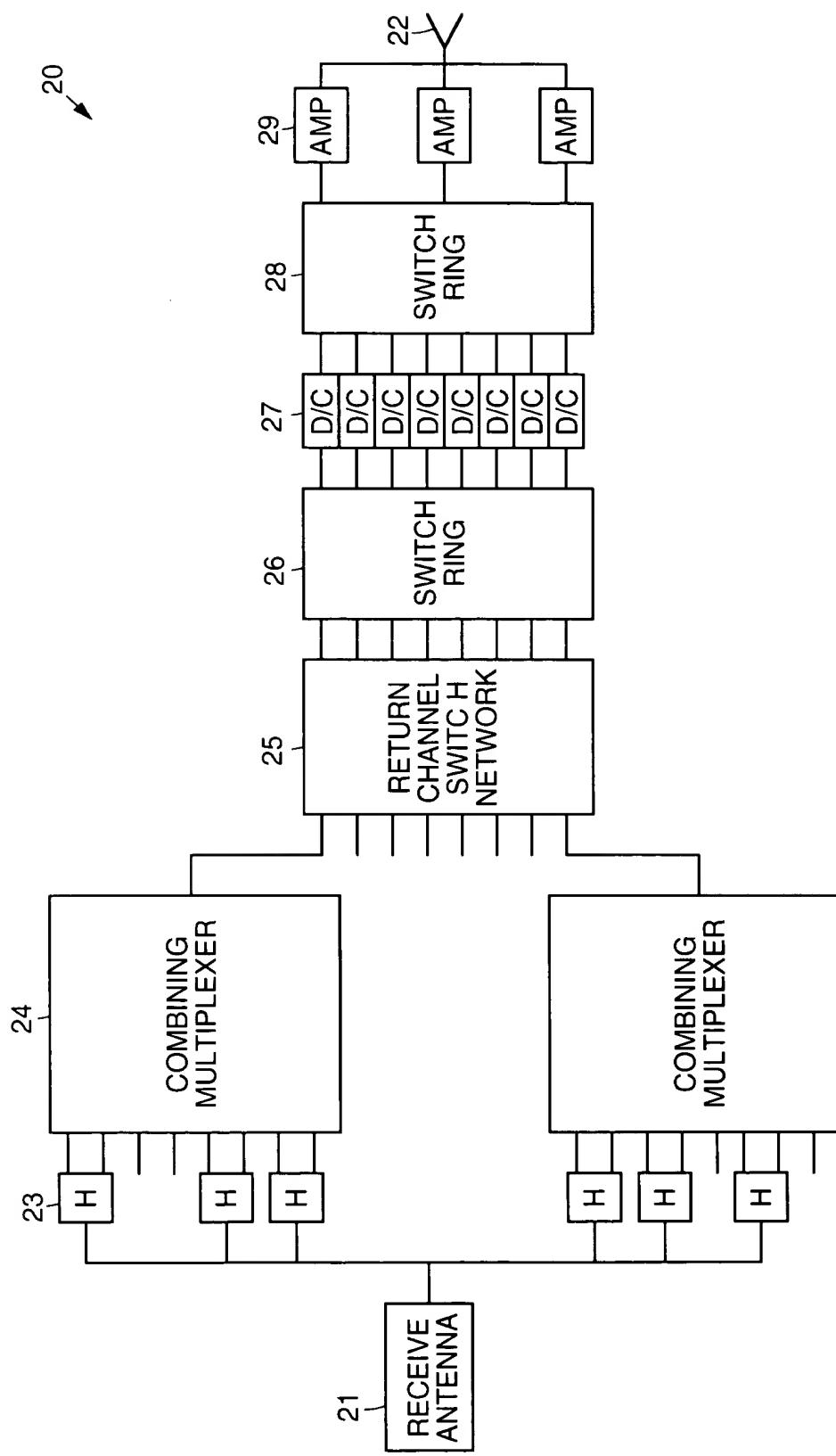


Fig. 2

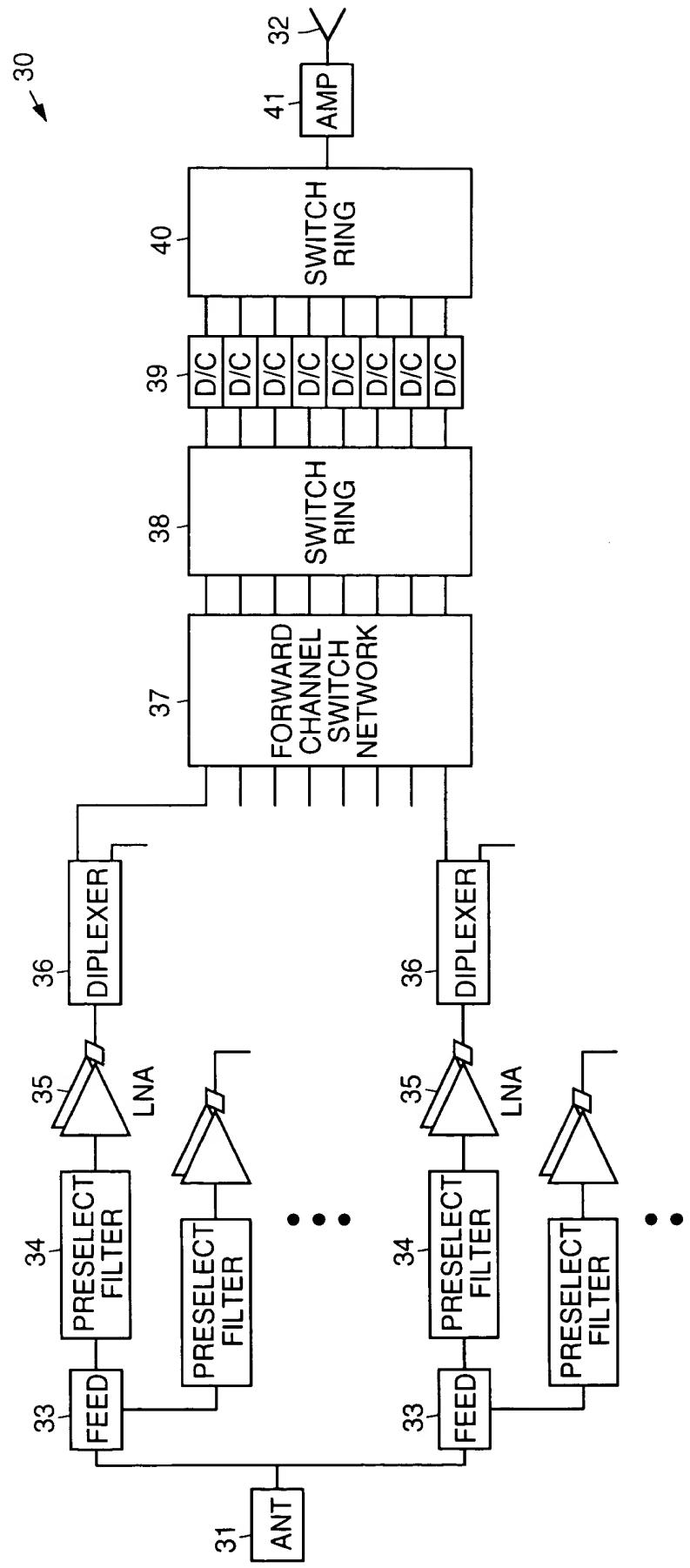


Fig. 3

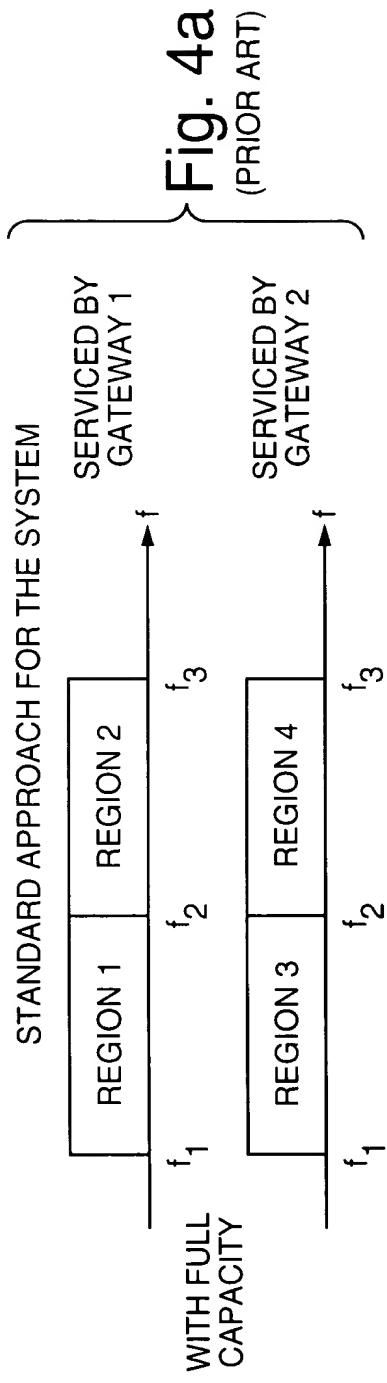


Fig. 4a
(PRIOR ART)

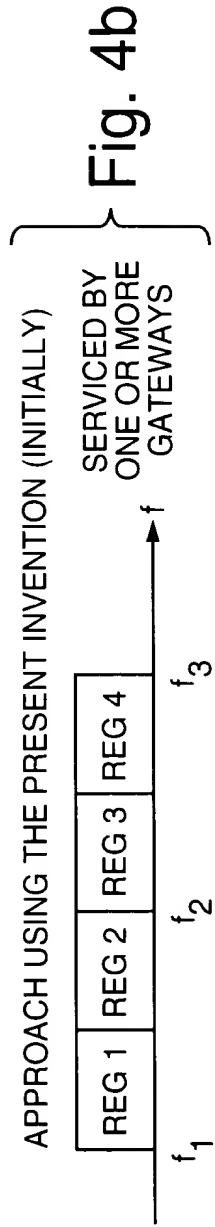


Fig. 4b

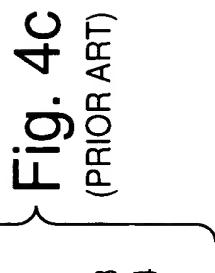
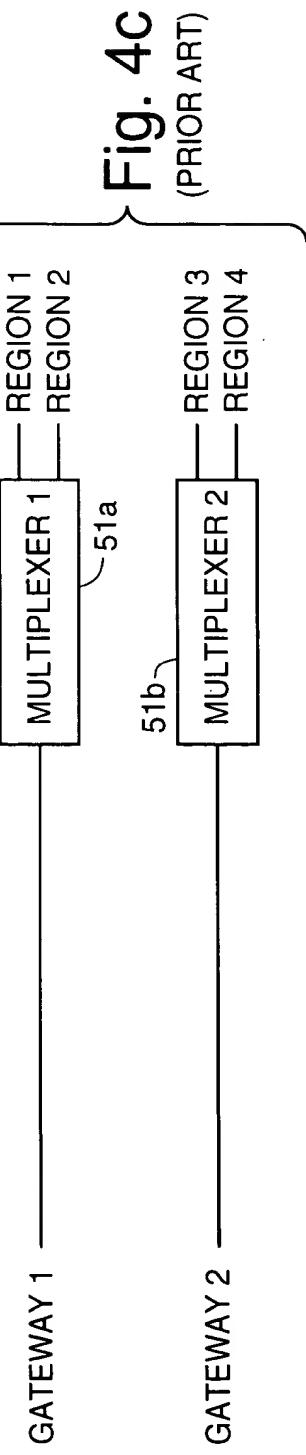
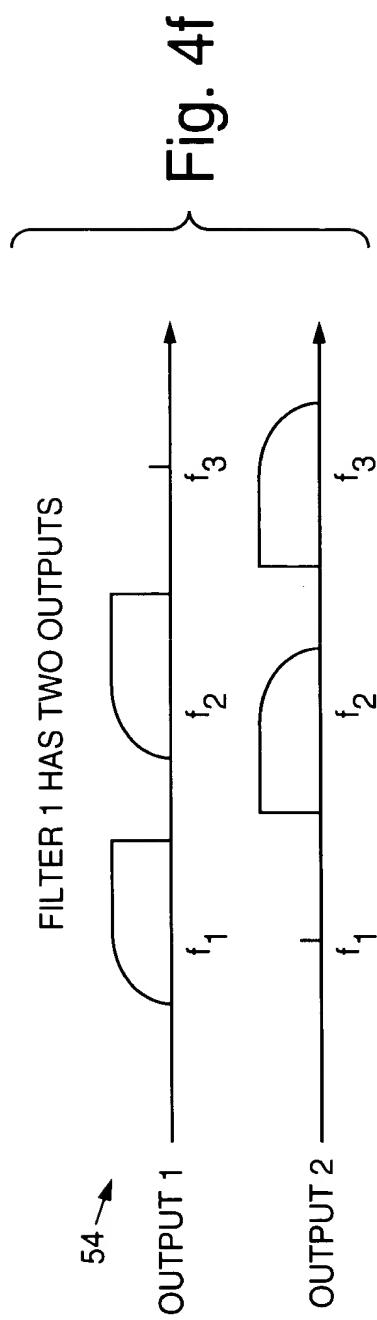
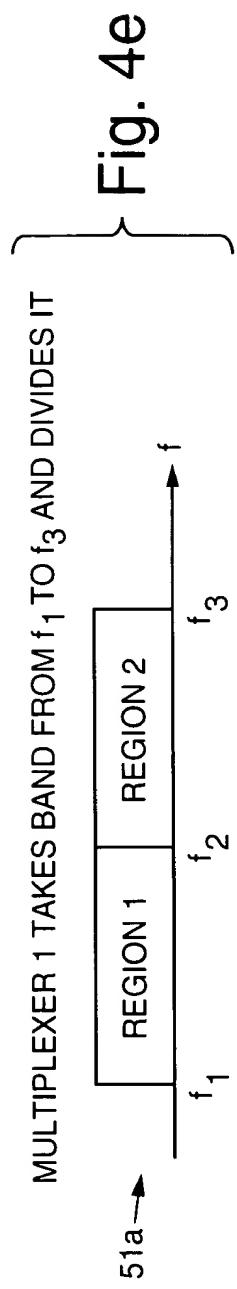
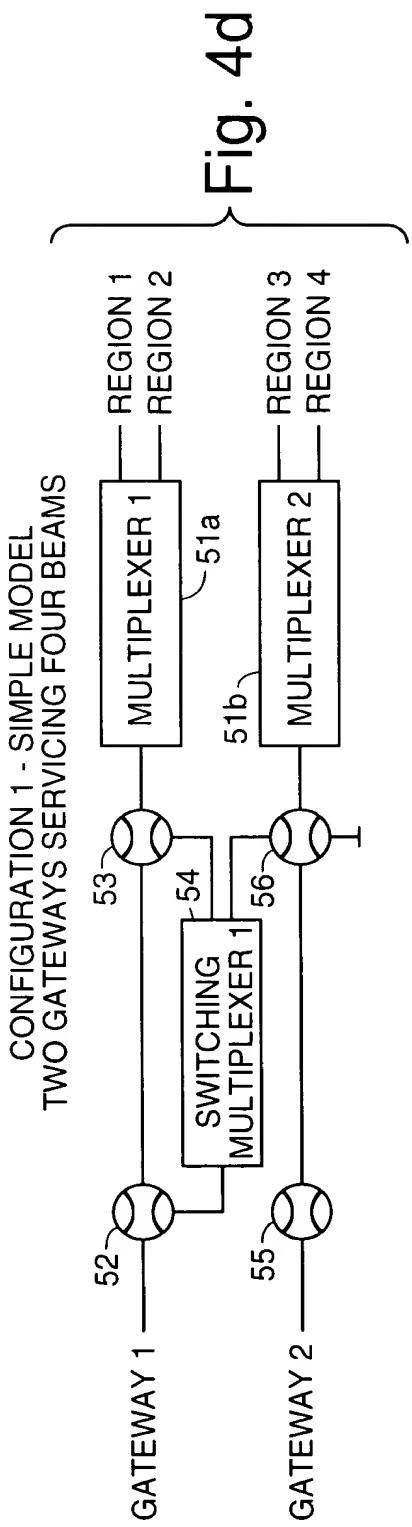


Fig. 4c
(PRIOR ART)



COMBINING OUTPUT 1 WITH MULTIPLEXER 1 GIVES

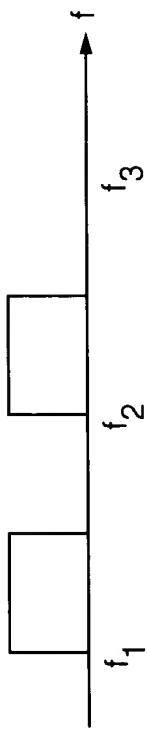


Fig. 4g

COMBINING OUTPUT 2 WITH MULTIPLEXER 1 GIVES

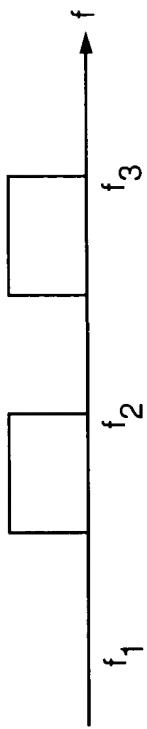


Fig. 4h

THE FILTER OUTPUT COMBINED WITH THE MULTIPLEXER
OUTPUT MEETS CHANNELIZATION REQUIREMENTS AND THE
SWITCH NETWORKS ROUTE THE BANDWIDTH APPROPRIATELY

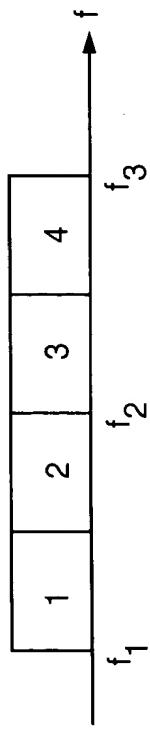


Fig. 4i

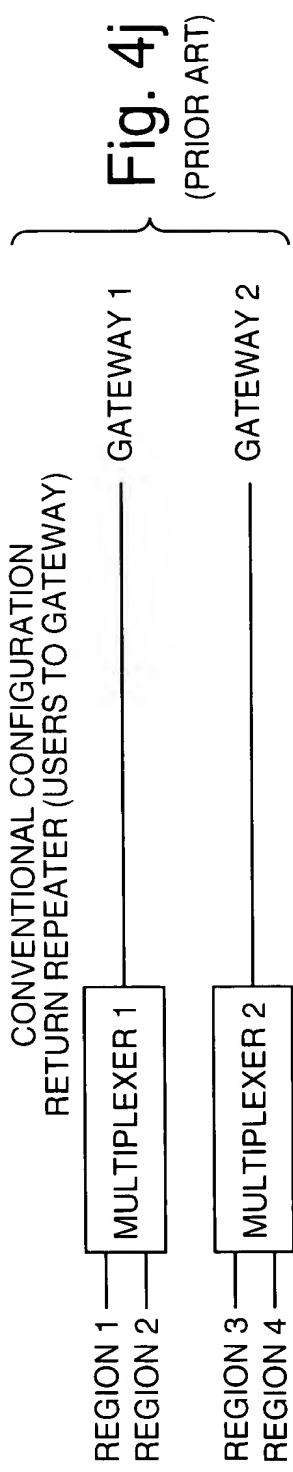


Fig. 4j

(PRIOR ART)

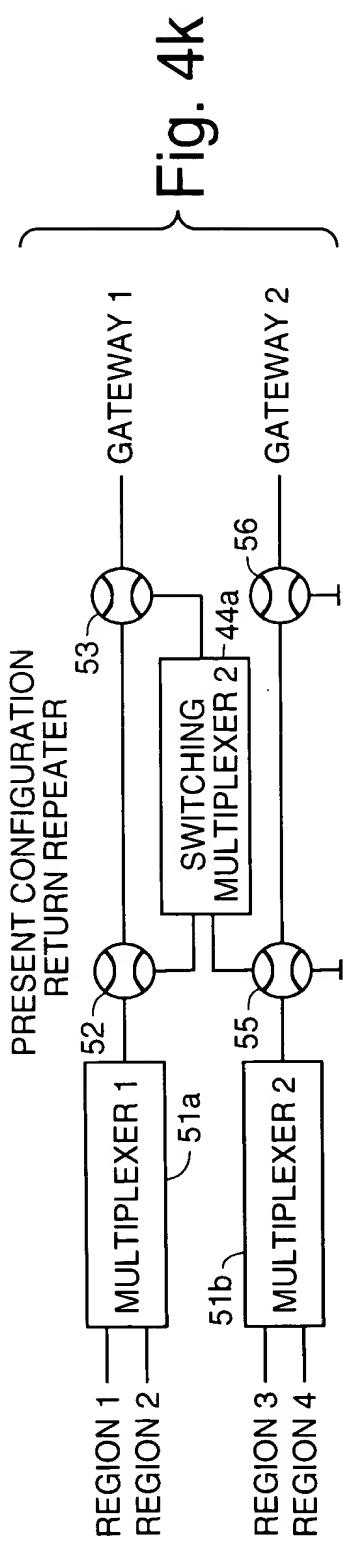


Fig. 4k

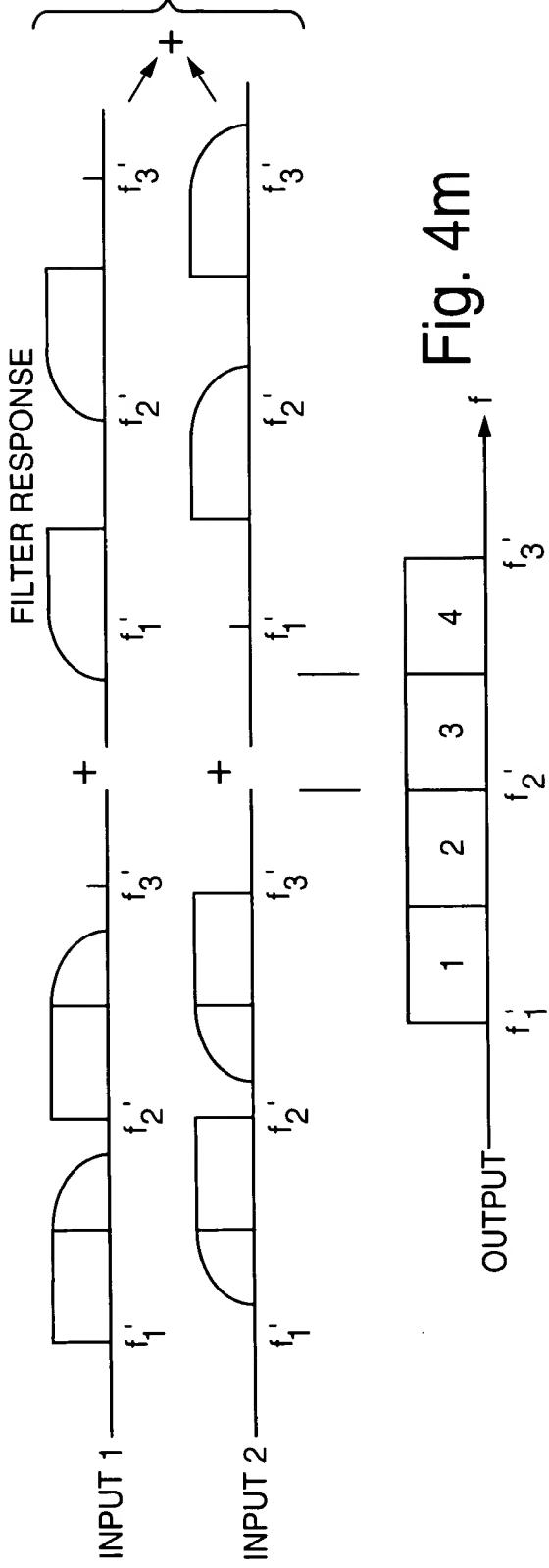


Fig. 4l

Fig. 4m

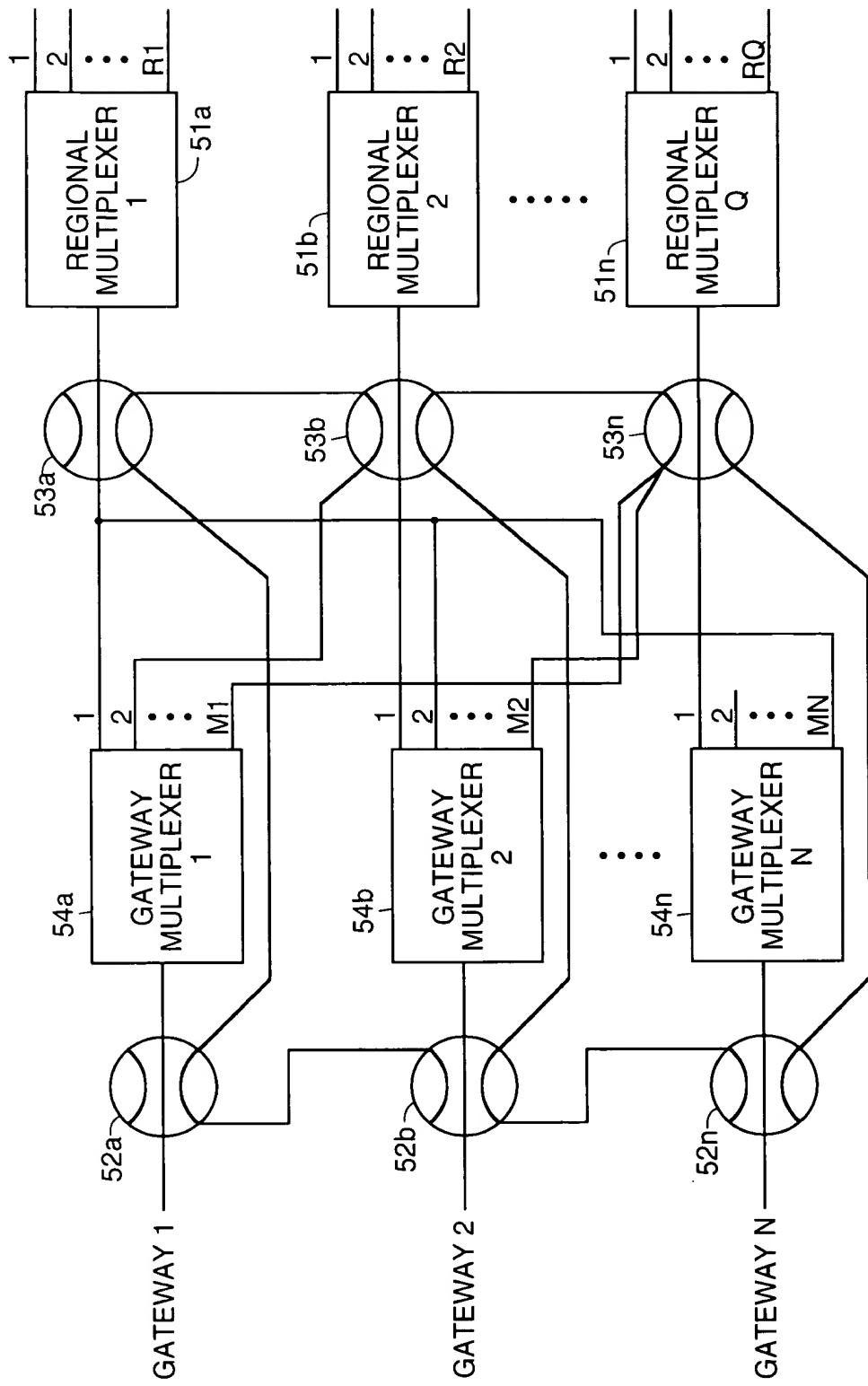


Fig. 5